

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:

10/054,653

Confirmation No.:

9448

First Named Inventor: Bulucea, Constantin

Filing Date:

18 January 2002 ·

Group Art Unit:

2814

Examiner:

Farahani, D.

Atty. Docket No.:

NS-5127 US

Title:

Gate-Enhanced Junction Varactor With Gradual

Capacitance Variation

Assignee(s):

National Semiconductor Corporation

Mountain View, California 11 June 2004

**COMMISSIONER FOR PATENTS** PO Box 1450 Alexandria, Virginia 22313-1450

## PETITION FOR EXTENSION OF TIME

Sir:

Attorney for Applicant(s) petitions for an extension of time of one (1) month within which to respond to the Office Action mailed 12 February 2004 for the above patent application, such extension allowing Attorney for Applicant(s) until 12 June 2004 to respond.

Payment of the extension fee of \$110.00 is provided for in the accompanying transmittal letter.

**EXPRESS MAIL LABEL NO.:** 

EV 337 115 897 US

Respectfully submitted,

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